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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

EXAMINER INITIAL		OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
JK	1	Guindi et al., "High-Level Analog Synthesis Using Signal Flow Graph Transformations", (1995) IEEE pgs 366-369.
JK	2	Grimm et al.., "Repartitioning and Technology Mapping of Electronic Hybrid Systems", (1998) IEEE pgs 52-58.
JK	3	Huijs, "A Graph Rewriting Approach for Transformational Design of Digital Systems", (1996) IEEE pgs 177-181.
JK	4	Cottrell, "Event-Driven Behavioral Simulation of Analogue Transfer Functions", (1990) IEEE pgs 240-243.
JK	5	Subramaniam et al., "Behavioral Modeling Techniques for Analog and Mixed Signal Design", (1994) IEEE pgs 319-322.

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EXAMINER	DATE CONSIDERED 3/2/05

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